

	CFP2 Bottom	CFP2 Bottom ALT1	CFP2 Bottom ALT2	CFP2 Top	CFP2 Top ALT1	CFP2 Top ALT2
1	GND	GND	GND	104	GND	GND
2	(TX_MCLKn)	TX9n	S3 TXn	103	N.C.	S1 TX3n
3	(TX_MCLKp)	TX9p	S3 TXp	102	N.C.	S1 TX3p
4	GND	GND	GND	101	GND	GND
5	N.C.	TX8n	S2 TXn	100	TX3n	S1 TX2n
6	N.C.	TX8p	S2 TXp	99	TX3p	S1 TX2p
7	3.3V GND	GND	GND	98	GND	GND
8	3.3V GND	3.3V GND	3.3V GND	97	TX2n	S1 TX1n
9	3.3V	3.3V	3.3V	96	TX2p	S1 TX1p
10	3.3V	3.3V	3.3V	95	GND	GND
11	3.3V	3.3V	3.3V	94	N.C.	S1 TX0n
12	3.3V	3.3V	3.3V	93	N.C.	S1 TX0p
13	3.3V GND	3.3V GND	3.3V GND	92	GND	GND
14	3.3V GND	3.3V GND	3.3V GND	91	N.C.	TX3n
15	VND IO A	VND IO A	VND IO A	90	N.C.	TX3p
16	VND IO B	VND IO B	VND IO B	89	GND	GND
17	PRG_CNTL1	PRG_CNTL1	PRG_CNTL1	88	TX1n	TX2n
18	PRG_CNTL2	PRG_CNTL2	PRG_CNTL2	87	TX1p	TX2p
19	PRG_CNTL3	PRG_CNTL3	PRG_CNTL3	86	GND	GND
20	PRG_ALRM1	PRG_ALRM1	PRG_ALRM1	85	TX0n	TX1n
21	PRG_ALRM2	PRG_ALRM2	PRG_ALRM2	84	TX0p	TX1p
22	PRG_ALRM3	PRG_ALRM3	PRG_ALRM3	83	GND	GND
23	GND	GND	GND	82	N.C.	TX0n
24	TX_DIS	TX_DIS	TX_DIS	81	N.C.	TX0p
25	RX_LOS	RX_LOS	RX_LOS	80	GND	GND
26	MOD_LOPWR	MOD_LOPWR	MOD_LOPWR	79	(REFCLKn)	(REFCLKn)
27	MOD_ABS	MOD_ABS	MOD_ABS	78	(REFCLKp)	(REFCLKp)
28	MOD_RSTn	MOD_RSTn	MOD_RSTn	77	GND	GND
29	GLB_ALRMn	GLB_ALRMn	GLB_ALRMn	76	N.C.	S1 RX3n
30	GND	GND	GND	75	N.C.	S1 RX3p
31	MDC	MDC	MDC	74	GND	GND
32	MDIO	MDIO	MDIO	73	RX3n	S1 RX2n
33	PRTADR0	PRTADR0	PRTADR0	72	RX3p	S1 RX2p
34	PRTADR1	PRTADR1	PRTADR1	71	GND	GND
35	PRTADR2	PRTADR2	PRTADR2	70	RX2n	S1 RX1n
36	VND IO C	VND IO C	VND IO C	69	RX2p	S1 RX1p
37	VND IO D	VND IO D	VND IO D	68	GND	GND
38	VND IO E	VND IO E	VND IO E	67	N.C.	S1 RX0n
39	3.3V GND	3.3V GND	3.3V GND	66	N.C.	S1 RX0p
40	3.3V GND	3.3V GND	3.3V GND	65	GND	GND
41	3.3V	3.3V	3.3V	64	N.C.	RX3n
42	3.3V	3.3V	3.3V	63	N.C.	RX3p
43	3.3V	3.3V	3.3V	62	GND	GND
44	3.3V	3.3V	3.3V	61	RX1n	RX2n
45	3.3V GND	3.3V GND	3.3V GND	60	RX1p	RX2p
46	3.3V GND	GND	GND	59	GND	GND
47	N.C.	RX9n	S3 RXn	58	RX0n	RX1n
48	N.C.	RX9p	S3 RXp	57	RX0p	RX1p
49	GND	GND	GND	56	GND	GND
50	(RX_MCLKn)	RX8n	S2 RXn	55	N.C.	RX0n
51	(RX_MCLKp)	RX8p	S2 RXp	54	N.C.	RX0p
52	GND	GND	GND	53	GND	GND

Toward Host



(pin views from top)

REFCLK
(Optional)

CFP4	
Bottom	
1	3.3V GND
2	3.3V GND
3	3.3V
4	3.3V
5	3.3V
6	3.3V
7	3.3V GND
8	3.3V GND
9	VND_IO_A
10	VND_IO_B
11	TX_DIS (PRG_CNTL 1)
12	RX_LOS (PRG_ALRM 1)
13	GLB_ALRMn
14	MOD_LOPWR
15	MOD_ABS
16	MOD_RSTn
17	MDC
18	MDIO
19	PRTADR0
20	PRTADR1
21	PRTADR2
22	VND_IO_C
23	VND_IO_D
24	VND_IO_E
25	GND
26	(MCLKn)
27	(MCLKp)
28	GND

CFP4	
Top	
56	GND
55	TX3n
54	TX3p
53	GND
52	TX2n
51	TX2p
50	GND
49	TX1n
48	TX1p
47	GND
46	TX0n
45	TX0p
44	GND
43	(REFCLKn)
42	(REFCLKp)
41	GND
40	RX3n
39	RX3p
38	GND
37	RX2n
36	RX2p
35	GND
34	RX1n
33	RX1p
32	GND
31	RX0n
30	RX0p
29	GND

CFP4	
Top ALT1	
GND	
TX0n	
TX0p	
GND	
TX1n	
TX1p	
GND	
TX2n	
TX2p	
GND	
TX3n	
TX3p	
GND	
(REFCLKn)	
(REFCLKp)	
GND	
RX3p	
RX3n	
GND	
RX2p	
RX2n	
GND	
RX1p	
RX1n	
GND	
RX0p	
RX0n	
GND	

Toward Host



(pin views from top)

REFCLK
(Optional)

MCLK = TX_MCLK +
RX_MCLK
(Optional)

TX_DIS (PRG_CNTL 1)

(Optionally configurable as Programmable Control after Reset)

RX_LOS (PRG_ALRM 1)

(Optionally configurable as Programmable Alarm after Reset)